

— DQ / DQS  
— CA  
— CLK  
— HS Diff-Pairs

Figure 1: "DIMM" reference model

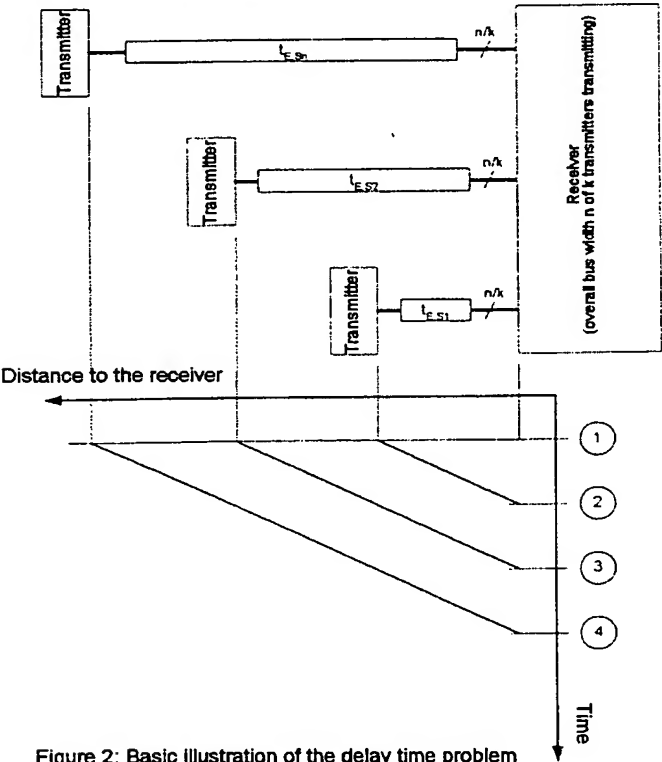


Figure 2: Basic illustration of the delay time problem

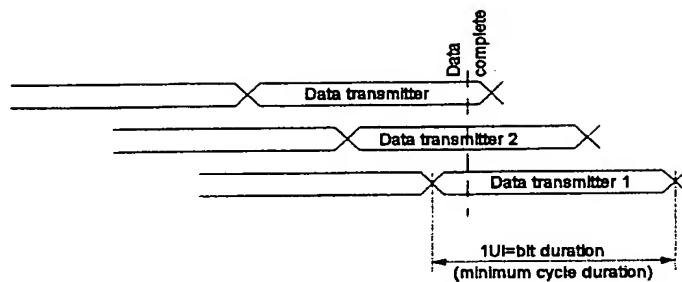


Figure 3: Danger to data integrity resulting from delay time differences

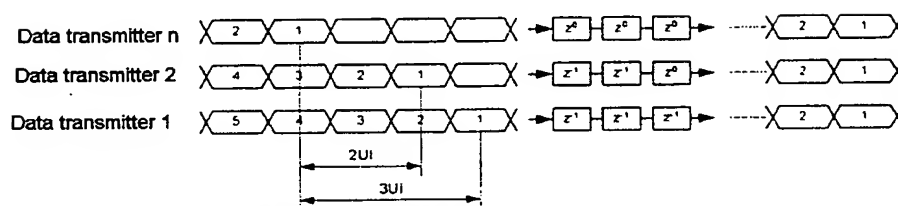


Figure 4: Principle of digital delay time compensation

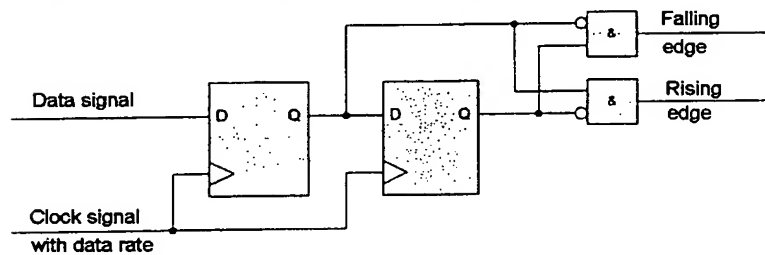


Figure 5: Edge detector

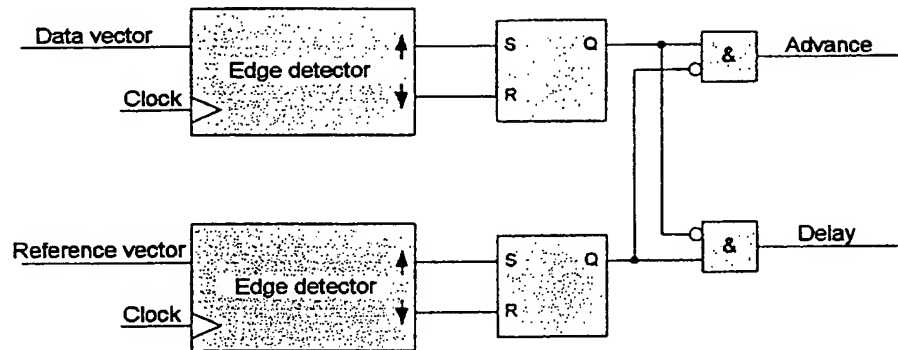


Figure 6: Arrangement for direction determination